O I P E
DEC 2 7 2004
PATENT & TRADEMARK OFFICE

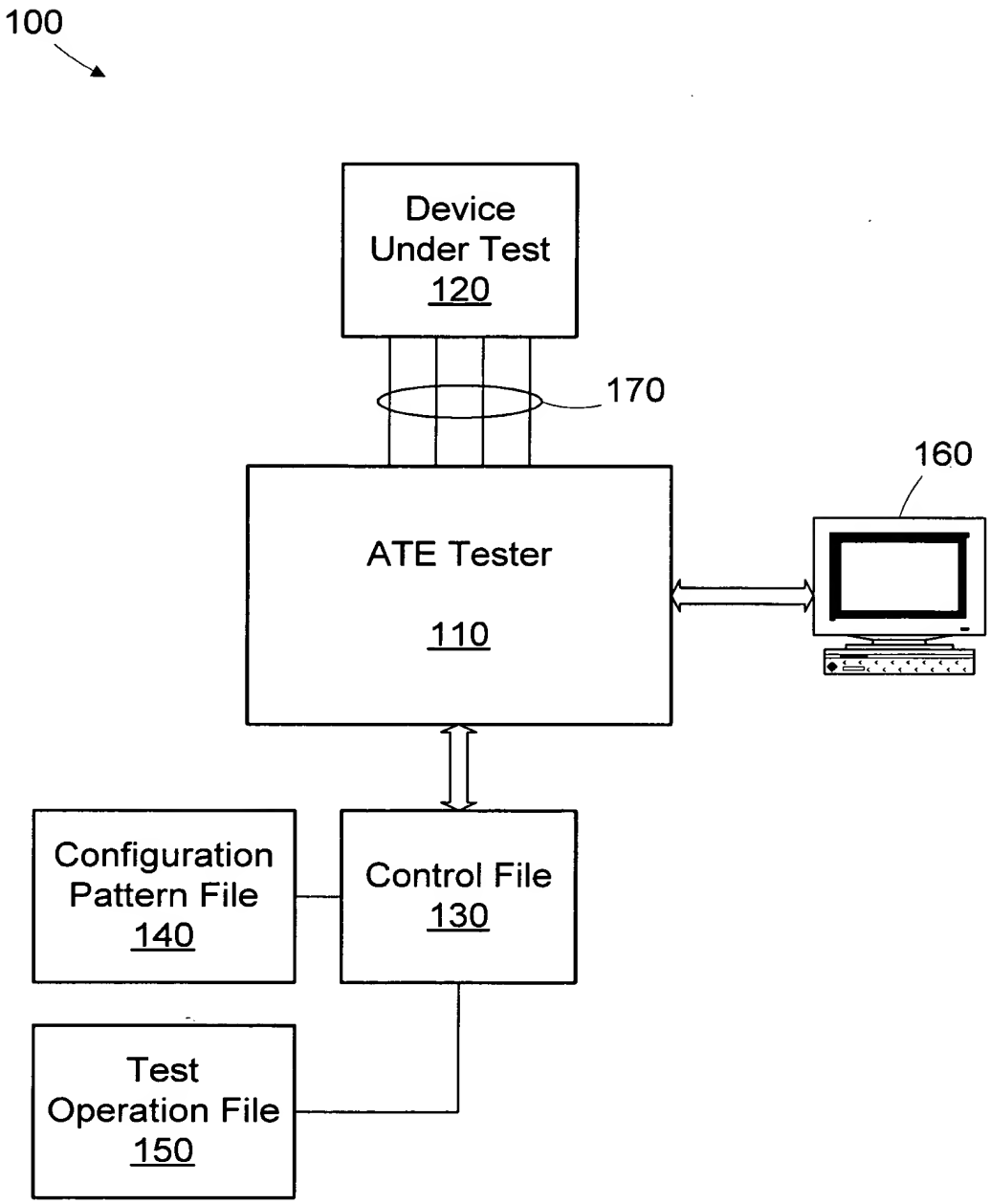

Figure 1

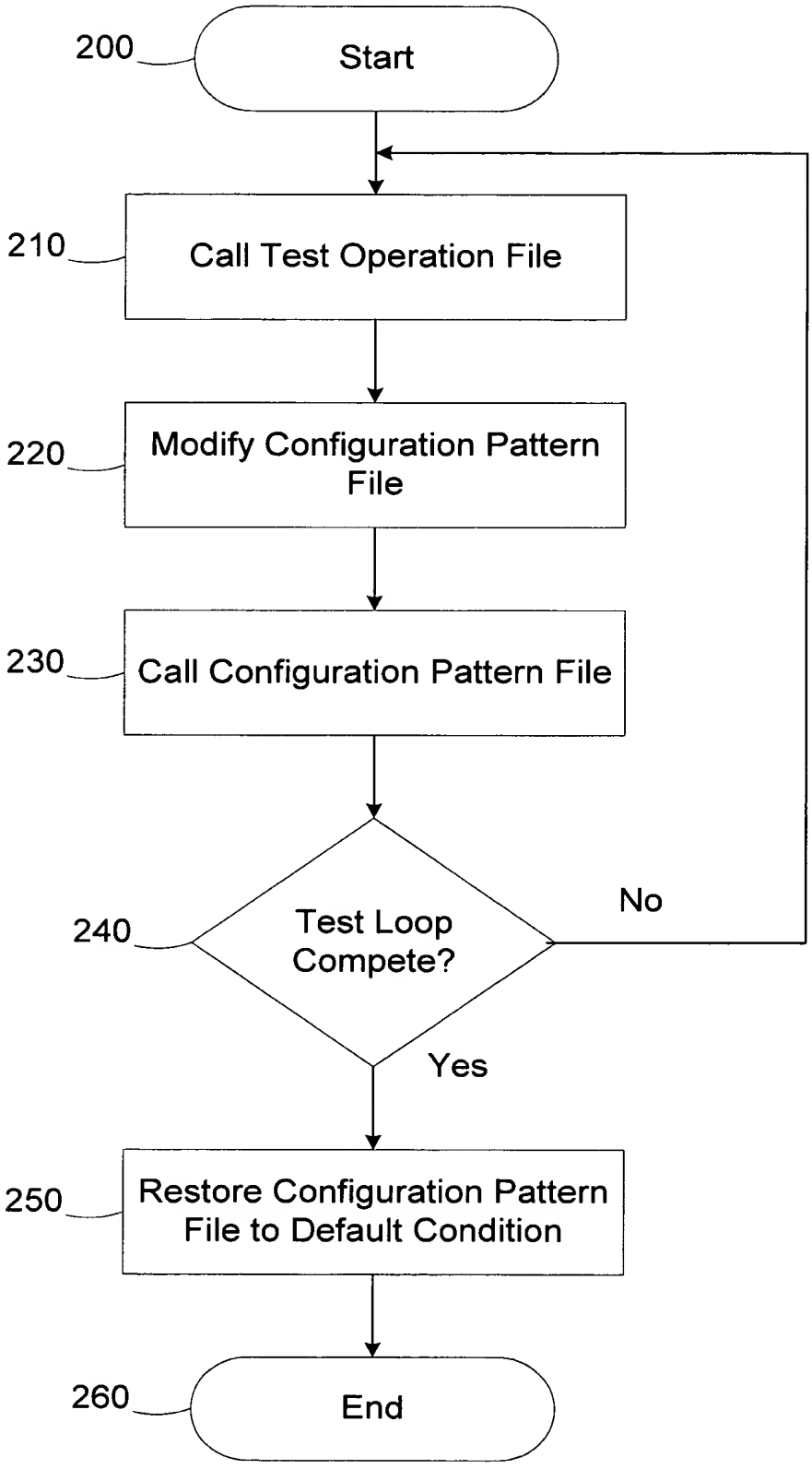
200
Start
210
Call Test Operation File
220
Modify Configuration Pattern File
230
Call Configuration Pattern File
240
Test Loop Compete?
No
Yes
250
Restore Configuration Pattern File to Default Condition
260
End

Figure 2

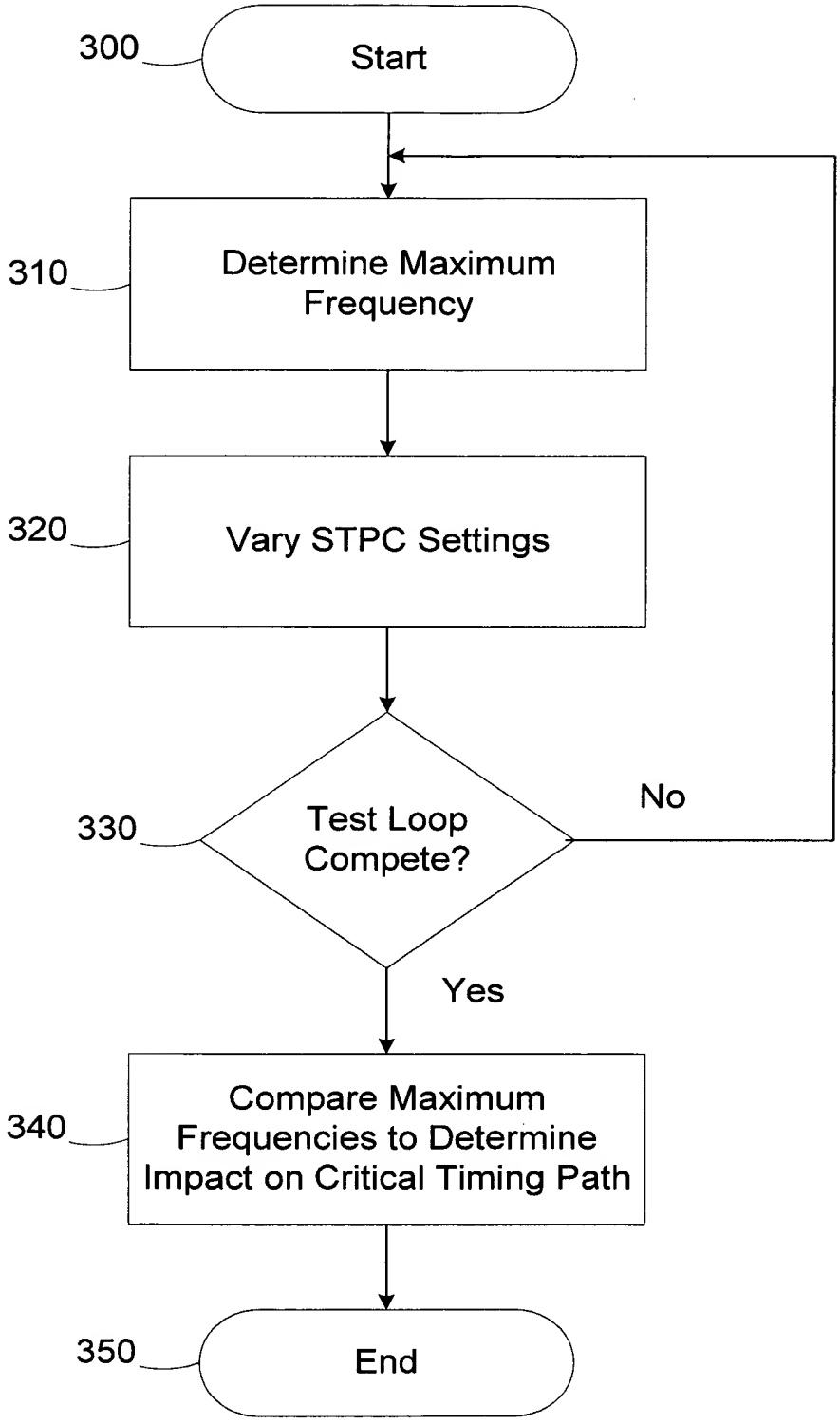
300
Start
310
Determine Maximum Frequency
320
Vary STPC Settings
330
Test Loop Compete?
No
Yes
340
Compare Maximum Frequencies to Determine Impact on Critical Timing Path
350
End

Figure 3

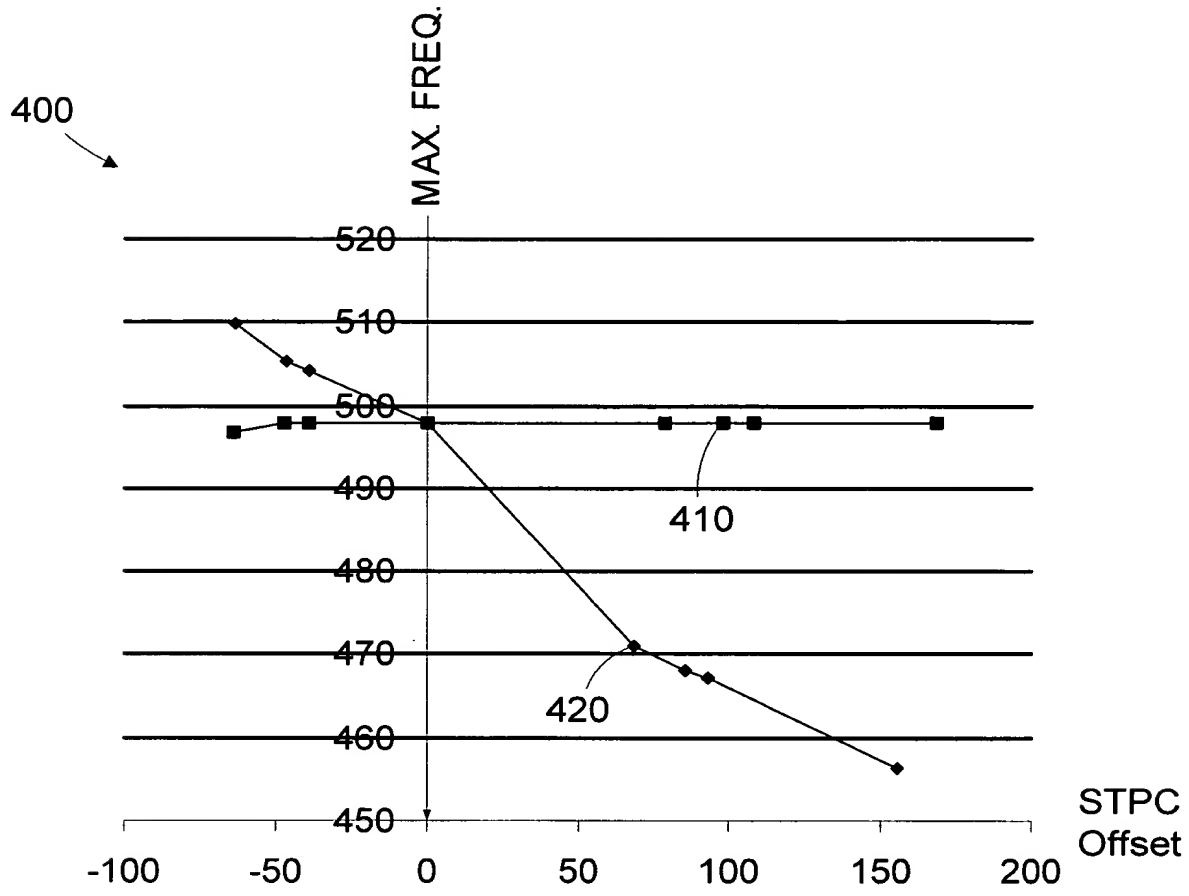
400
MAX. FREQ.
520
510
500
490
480
470
460
450
410
420
-100
-50
0
50
100
150
200
STPC
Offset

Figure 4

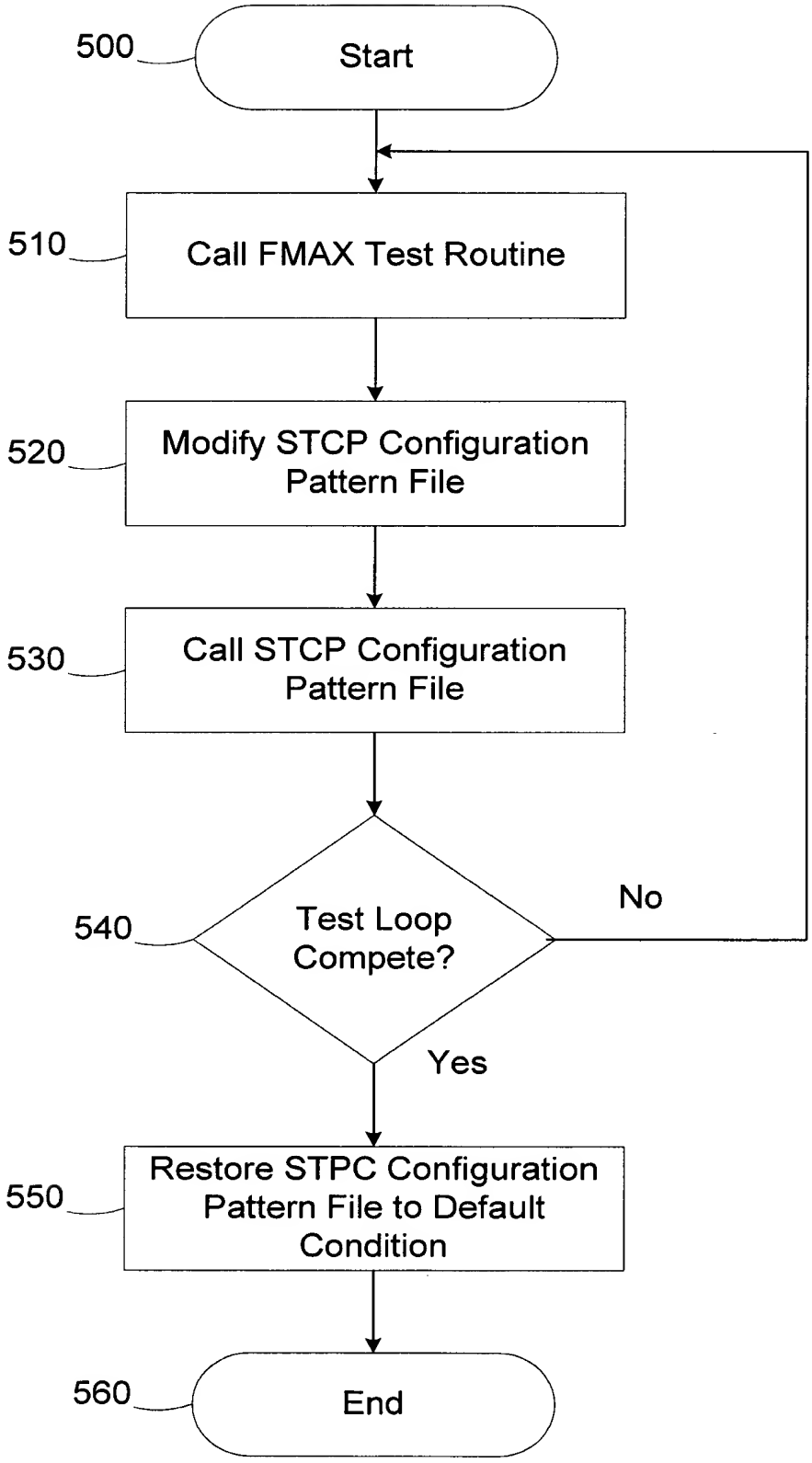
500
Start
510
Call FMAX Test Routine
520
Modify STCP Configuration Pattern File
530
Call STCP Configuration Pattern File
540
Test Loop Compete?
No
Yes
550
Restore STPC Configuration Pattern File to Default Condition
560
End

Figure 5